

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Urs Bapst
Christoph Berger
Russell Alan Budd
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Docket No.: YOR920030633US1

Serial No.: Unassigned

Filing Date: Concurrently Herewith

Title: **CIRCUIT BOARD INTEGRATED OPTICAL COUPLING ELEMENTS**

INFORMATION DISCLOSURE STATEMENT

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicant's attorney wishes to bring to the attention of the Patent and Trademark Office the following document(s) listed on the accompanying PTO Form 1449.

U.S. Patent Documents

1. U.S. Patent No. 6,236,788
2. U.S. Patent Application No. 2003-0021541 A1

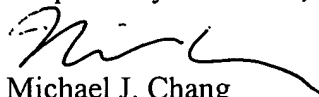
Copies of each of the following listed items are enclosed:

Other Documents

3. Miller, D.A.B., "Rationale and Challenges for Optical Interconnects to Electronic Chips," Proceedings of the IEEE, Vol. 88, No. 6, Pgs. 728-749 (June 2000).
4. Nordin et al., "A Systems Perspective on Digital Interconnection Technology," Journal of Lightwave Technology, Vol. 10, No. 6, Pgs. 811-827 (June 1992).

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability or that no other material information exists.

Respectfully submitted,



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Date: February 10, 2004

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**LIST OF PUBLICATIONS FOR
APPLICANT'S INFORMATION
DISCLOSURE STATEMENT**

U.S. PATENT DOCUMENTS

EXAMINER					FILING DATE
INITIAL	DOCUMENT NO.	DATE	NAME	CLASS/SUBCLASS	IF APPROPRIATE
_____	6,236,788	05/22/01	Moisel	385/52	
_____	20030021541	01/30/03	Carpenter	385/52	07/24/01

FOREIGN PATENT DOCUMENTS

EXAMINER					TRANSLATION
INITIAL	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS	YES NO

OTHER DOCUMENTS

EXAMINER			
INITIAL	REF NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.	
_____		Miller, D.A.B., "Rationale and Challenges for Optical Interconnects to Electronic Chips," Proceedings of the IEEE, Vol. 88, No. 6, Pgs. 728-749 (June 2000).	
_____		Nordin et al., "A Systems Perspective on Digital Interconnection Technology," Journal of Lightwave Technology, Vol. 10, No. 6, Pgs. 811-827 (June 1992).	

Examiner

Date Considered

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.